



PATENT APPLICATION
Docket No. 2522-022
Client No. AW8030US/SY

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Seung-Jae Lee and Jin-Gi Hong
Serial No. 10/626,096 Art Unit: 2818
Filed: July 23, 2004 Examiner: not yet assigned
For: GATE ELECTRODE OF A SEMICONDUCTOR DEVICE AND
METHOD OF FORMING THE SAME
Confirmation No. 9000

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

☒ under 37 CFR 1.97 (b); or
(Within three months of filing national application; or date of entry of International application; or before mailing date of first office action on the merits; whichever occurs last)

☒ Applicant(s) submit herewith Form PTO 1449- Information Disclosure Citation together with copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

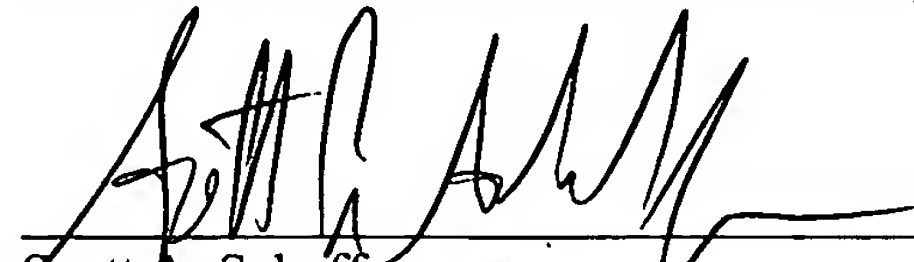
It is requested that the information disclosed herein be made of record in this application.

Any deficiency or overpayment should be charged or credited to deposit account number 13-1703. A duplicate copy of this sheet is enclosed.

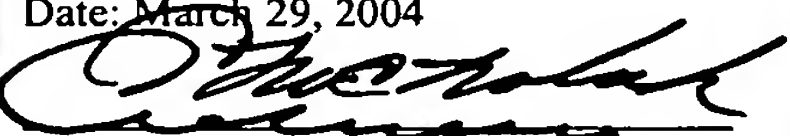
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Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450
Date: March 29, 2004

Adrienne Chocholak



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**INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)**

U.S. PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Issue</u> <u>Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub</u> <u>Class</u>
_____	_____	5,877,074	5/2/1999	Jeng, et al.		

Examiner: _____

Date Considered: _____